Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

• Extremely Fast Switching Speed

Rating

Forward Power Dissipation

Non-Repetitive Peak Forward

t_p < 10 msec Repetitive Peak Forward Current

Pulse Wave = 1 sec, Duty Cycle

Storage Temperature Range

Derate above 25°C

Forward Current (DC)

Junction Temperature

Reverse Voltage

@ T_A = 25°C

Current

= 66%

- Low Forward Voltage 0.35 Volts (Typ) @ $I_F = 10 \text{ mAdc}$
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Symbol

VR

 P_{F}

 I_{F}

I_{FSM}

IFRM

ТJ

T_{stg}

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the

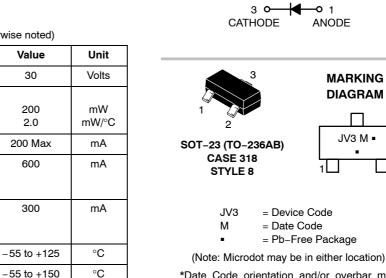
Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

http://onsemi.com

30 VOLTS SILICON HOT-CARRIER DETECTOR AND SWITCHING DIODES



*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

| Device | Package | Shipping [†] | | |
|-----------|---------------------|-----------------------|--|--|
| BAT54LT1G | SOT-23 (Pb-Free) | 3000/Tape & Reel | | |

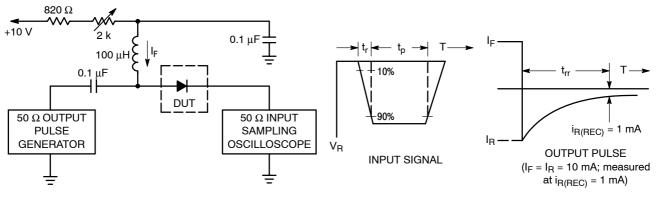
+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Semiconductor Components Industries, LLC, 2010 August, 2010 – Rev. 10

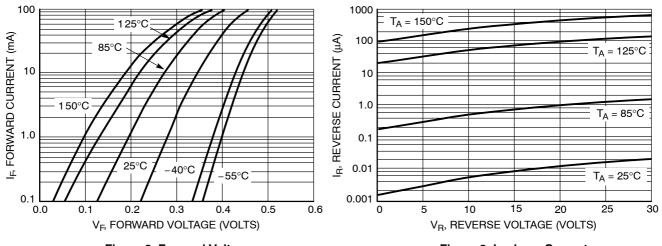
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|--------------------|-----|------|------|-------|
| Reverse Breakdown Voltage (I _R = 10 µA) | V _{(BR)R} | 30 | - | - | Volts |
| Total Capacitance (V _R = 1.0 V, f = 1.0 MHz) | CT | - | 7.6 | 10 | pF |
| Reverse Leakage (V _R = 25 V) | I _R | - | 0.5 | 2.0 | μAdc |
| Forward Voltage (I _F = 0.1 mAdc) | V _F | - | 0.22 | 0.24 | Vdc |
| Forward Voltage (I _F = 30 mAdc) | V _F | - | 0.41 | 0.5 | Vdc |
| Forward Voltage (I _F = 100 mAdc) | V _F | - | 0.52 | 0.8 | Vdc |
| Reverse Recovery Time (I _F = I _R = 10 mAdc, I _{R(REC)} = 1.0 mAdc, Figure 1) | t _{rr} | - | - | 5.0 | ns |
| Forward Voltage (I _F = 1.0 mAdc) | V _F | - | 0.29 | 0.32 | Vdc |
| Forward Voltage (I _F = 10 mAdc) | V _F | - | 0.35 | 0.40 | Vdc |



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA. 3. t_p » t_{rr}





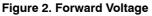


Figure 3. Leakage Current

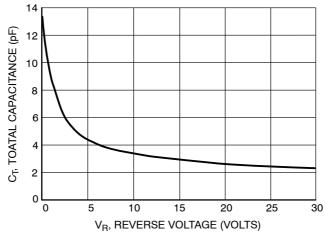
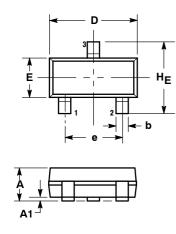
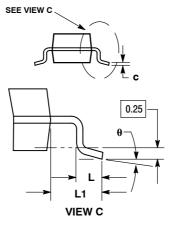


Figure 4. Total Capacitance

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AP





NOTES:

- DIBENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
- THICKNESS OF BASE MATERIAL. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- 4 PROTRUSIONS, OR GATE BURRS.

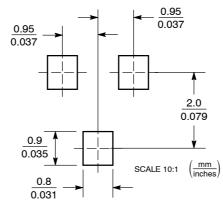
| | М | ILLIMETE | RS | INCHES | | | |
|-----|------|----------|------|--------|-------|-------|--|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX | |
| Α | 0.89 | 1.00 | 1.11 | 0.035 | 0.040 | 0.044 | |
| A1 | 0.01 | 0.06 | 0.10 | 0.001 | 0.002 | 0.004 | |
| b | 0.37 | 0.44 | 0.50 | 0.015 | 0.018 | 0.020 | |
| c | 0.09 | 0.13 | 0.18 | 0.003 | 0.005 | 0.007 | |
| D | 2.80 | 2.90 | 3.04 | 0.110 | 0.114 | 0.120 | |
| Е | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 | |
| е | 1.78 | 1.90 | 2.04 | 0.070 | 0.075 | 0.081 | |
| L | 0.10 | 0.20 | 0.30 | 0.004 | 0.008 | 0.012 | |
| L1 | 0.35 | 0.54 | 0.69 | 0.014 | 0.021 | 0.029 | |
| ΗE | 2.10 | 2.40 | 2.64 | 0.083 | 0.094 | 0.104 | |
| θ | 0° | | 10° | 0° | | 10° | |

STYLE 8:

PIN 1. ANODE 2. NO CONNECTION

З. CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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